

Gayatri Vidya Parishad College of Engineering for Women
Madhurawada, Visakhapatnam, 530048
(Affiliated to JNTUK, Approved by AICTE, New Delhi)
Department of Electrical and Electronics Engineering
Electrical Engineering
Branch-I Semester Regular External Lab Exam Time Table February-2022
Branch: Electrical and Electronics Engineering

ROLL NUMBERS			TOTAL	TIMINGS	ROOM	FACULTY
FROM	TO	EXCEPT				
20JG1A0201	20JG1A0219	20JG1A0209	18	9:00AM-12:00PM	Electrical Machines Lab	Mr DSR/ChV
20JG1A0220	20JG1A0231	20JG1A0223	17	11:00AM-2:00PM		
21JG5A0201	21JG5A0206		17	1:00PM-4:00PM		
21JG5A0207	21JG5A0223					
20JG1A0201	20JG1A0219	20JG1A0209	18	9:00AM-12:00PM	EDC Lab	Mrs LS/Mrs BDS
20JG1A0220	20JG1A0231	20JG1A0223	17	11:00AM-2:00PM		
21JG5A0201	21JG5A0206		17	1:00PM-4:00PM		
21JG5A0207	21JG5A0223					
20JG1A0201	20JG1A0219	20JG1A0209	18	9:00AM-12:00PM	Electrical Circuits Lab	Mrs VSV/Dr RVSL
20JG1A0220	20JG1A0231	20JG1A0223	17	11:00AM-2:00PM		
21JG5A0201	21JG5A0206		17	1:00PM-4:00PM		
21JG5A0207	21JG5A0223					
20JG1A0201	20JG1A0231	20JG1A0209,223	52	9:00AM-12:00PM	314	Mr MK
21JG5A0201	21JG5A0223					

[Signature]
PRINCIPAL

Principal

G.V.P. College of Engineering for Women
 Madhurawada
 Visakhapatnam-530048

Electrical Engineering
 for Women
 a
 530 048

Tri Vidya Parishad College of Engineering for Women
Madhurawada, Visakhapatnam, 530048
Affiliated to JNTUK, Approved by AICTE, New Delhi)
Department of Electrical and Electronics Engineering
3rd-II Semester Supplementary External Lab Exam Time Table
Branch: Electrical and Electronics Engineering

STUDENT NAME	Roll Number	TOTAL	TIMINGS	ROOM	FACULTY
M-I LAB	19JG1A0224	1	9:00AM - 12:00PM	Machines Lab	Mr MK
E LAB	19JG5A0209	1	9:00AM - 12:00 PM	Power Electronics Lab	Mrs VSV


PRINCIPAL
Principal

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